

## Features

- Hybrid 6-pin package
- 1500Vdc isolation voltage
- CMR transient immunity >1000V/us
- Small package outline
- High reliability and rugged construction
- High reliability screening available
- DC input with on/off threshold hysteresis output
- Fast switching times:  $t_r, t_f = 10\text{ns}$  typical
- Operating temperature range  $-55^\circ\text{C}$  to  $+125^\circ\text{C}$

## Applications

- Switch mode power supplies
- Computer peripheral interface
- Motor control
- Ground signal isolation

## Description

The IBI600 consists of an integrated high-speed detector optically coupled to an AlGaAs infrared-emitting diode in a leadless hybrid surface mount package.

## Schematic Diagram

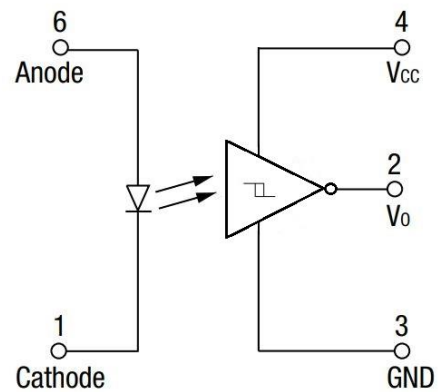


Figure 1. IBI600 Schematic Diagram

## Package Dimensions in inches (mm)

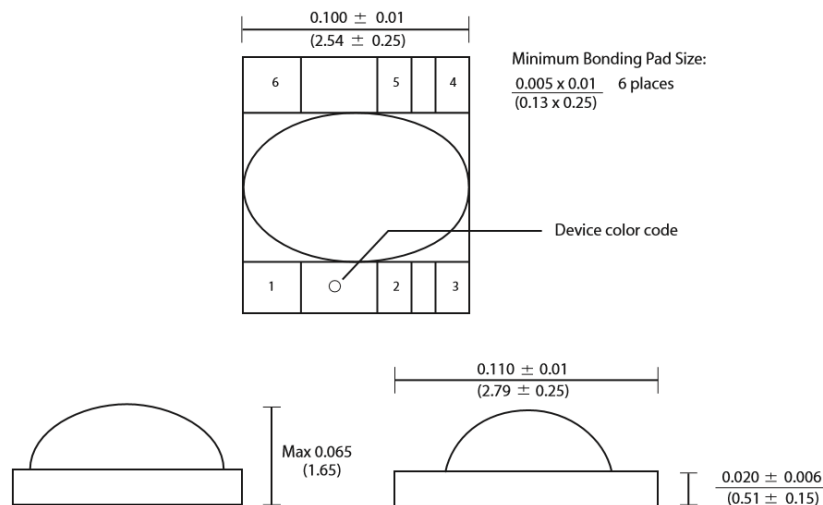


Figure 2. IBI600 Package Dimensions

**Absolute Maximum Rating at 25°C** (Note 1)

<b>Symbol</b>	<b>Parameters</b>	<b>Ratings</b>	<b>Units</b>	<b>Notes</b>
V <sub>DC</sub>	Isolation voltage	-1500 to +1500	V	2
T <sub>OPR</sub>	Operating temperature	-55 to +125	°C	
T <sub>STG</sub>	Storage temperature	-65 to +150	°C	
T <sub>SOL</sub>	Soldering temperature (10 seconds maximum)	240	°C	
P <sub>D</sub>	Total power dissipation	250	mW	3
<b>Emitter</b>				
I <sub>DD</sub>	Average input current	20	mA	
V <sub>R</sub>	Reverse voltage	5	V	
P <sub>D</sub>	Power Dissipation	36	mW	3
<b>Detector</b>				
V <sub>CC</sub>	Supply voltage	18	V	
V <sub>OUT</sub>	Output voltage	18	V	
I <sub>OUT</sub>	Peak output current	40	mA	

**Notes**

1. When using this product, please observe the absolute maximum ratings. Only one parameter may be set at the limit to ensure no damage to the device. Exceeding any of the limits listed here may damage the device.
2. Measured between input pins 1 and 6 shorted together, and output pins 2, 3, 4, and 5 shorted together. T<sub>A</sub> = 25°C and duration = 1sec.
3. Linear derating factor: 3.0 mW/°C above 25°C

**ESD Precaution**

Please be advised that normal static precautions should be taken in the handling and assembly of this device to prevent damage or degradation which may be induced by electrostatic discharge (ESD).

**Electrical Characteristics**  $T_A = -55^\circ\text{C}$  to  $+125^\circ\text{C}$  (unless otherwise specified) (Note 1)

**Emitter Characteristics**

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
$V_F$	Forward Voltage	$I_F=10\text{mA}$	-	1.6	2.4	V	
$I_R$	Reverse Current	$V_R = 3\text{V}$	-	-	10	$\mu\text{A}$	

**Detector Characteristics**

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
$I_{F(ON)}$	Threshold Current, ON	$V_{CC}=15\text{V}$ , $R_L=680\Omega$	-	-	10	mA	
$I_{F(OFF)} / I_{F(ON)}$	Hysteresis Ratio	$V_{CC}=15\text{V}$ , $R_L=680\Omega$	-	0.8	-	-	
$V_{OL}$	Low Level Output Voltage	$V_{CC}=15\text{V}$ , $R_L=680\Omega$ , $I_F=5\text{mA}$	5	-	-	V	
$I_{OH}$	High Level Output Current	$V_{CC}=V_O=15\text{V}$ , $I_F=0\text{mA}$			250	$\mu\text{A}$	
$I_{CCH}$	High Level Supply Current	$V_{CC}=15\text{V}$ , $I_F=0\text{mA}$	-	9	15	mA	
$I_{CCL}$	Low Level Supply Current	$V_{CC}=15\text{V}$ , $I_F=10\text{mA}$	-	9	15	mA	

**Common Mode Transient Immunity**

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
$CM_H$	Logic High	$V_{CM}=300\text{V peak}$ , $R_L=680\Omega$ , $I_F=0\text{mA}$ , $V_{CC}=15\text{V}$ , $T_A=25^\circ\text{C}$	1000	>10,000	-	V/ $\mu\text{s}$	
$CM_L$	Logic Low	$V_{CM}=300\text{V peak}$ , $R_L=680\Omega$ , $I_F=5\text{mA}$ , $V_{CC}=15\text{V}$ , $T_A=25^\circ\text{C}$	1000	>10,000	-	V/ $\mu\text{s}$	
$I_{L_O}$	Output Leakage Current	$V_{L_O}=1500\text{V}_{DC}$ , $R_H\leq 50\%$ , $T_A=25^\circ\text{C}$	-	-	1.0	$\mu\text{A}$	2

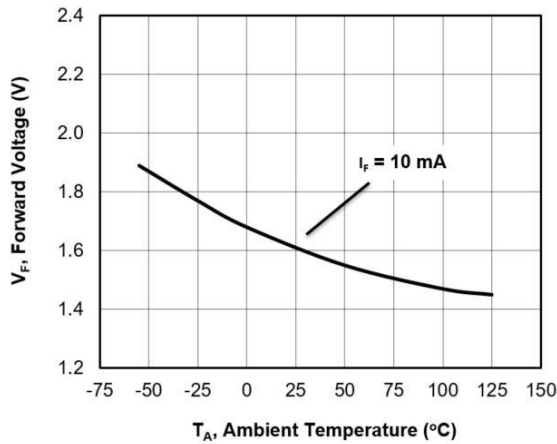
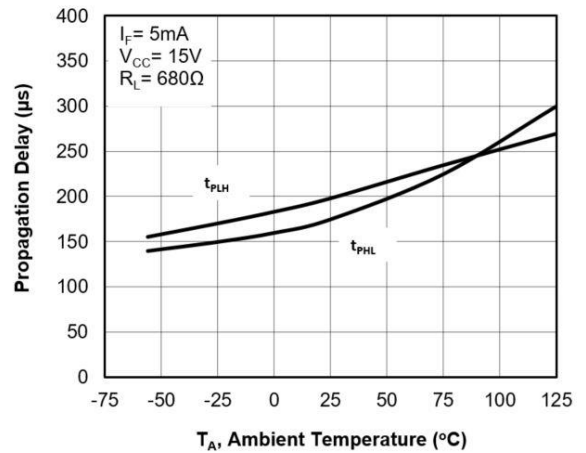
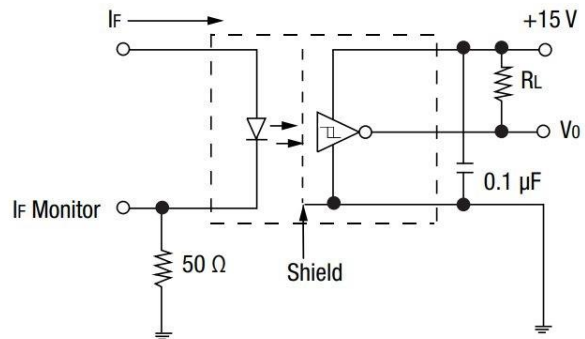
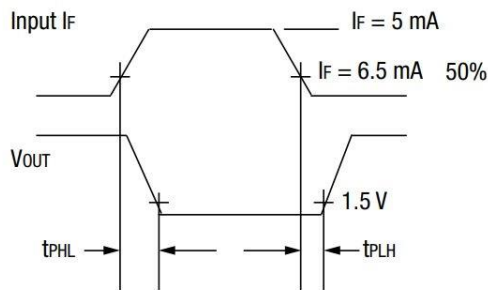
**Switching Characteristics**

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
$t_{PHL}$	Logic High to Low	$I_F=5\text{mA}$ , $V_{CC}=15\text{V}$ , $R_L=680\Omega$ , $T_A=25^\circ\text{C}$	-	-	300	ns	3
$t_{PLH}$	Logic Low to High		-	-	300		3
$t_r$	Rise Time		-	10	-		
$t_f$	Fall Time		-	10	-		

**Notes**

- Performance guaranteed only under conditions listed in above tables.
- Measured between input pins 1 and 6 shorted together, and output pins 2, 3, 4, and 5 shorted together.  $T_A = 25^\circ\text{C}$  and duration = 1sec.
- A ceramic bypass capacitor (0.01 $\mu\text{F}$  to 0.1 $\mu\text{F}$ ) between pins 3 and 5 is required to stabilize the operation of the amplifier.

### Typical Characteristic Curves


**Figure 3. Forward Voltage vs Temperature**

**Figure 4. Propagation Delay vs Temperature**

**Figure 5. IBI600 Switching Test Circuit**



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## Ordering Information

<i>Manufacturing Part Number</i>	<i>Part Description</i>
IBI600	Miniature High-Speed Schmitt Trigger 6-pin Hybrid Package

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